


ISSUE CLASSIFICATION	
Class	Subclass
	

PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

**PATENT DATE**

710  
SCANNED

**Q.A.**

APPLICATION NO. 09/805811	CONT/PRIOR F	CLASS 324 702	SUBCLASS 705/117	ART UNIT -2862- 2831	EXAMINER BARAN MELLALIE
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TITLE	APPLICANTS
1. <b>Chairman</b>	1. <b>Mr. J. Edgar Hoover</b>
2. <b>Vice Chairman</b>	2. <b>Mr. J. Edgar Hoover</b>
3. <b>Secretary</b>	3. <b>Mr. J. Edgar Hoover</b>
4. <b>Member</b>	4. <b>Mr. J. Edgar Hoover</b>
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Saburo Uehara

Semiconductor device testing apparatus and test method therefor

PTO-2040  
12/99

# ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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